

ABSTRACT OF THE DISCLOSURE

The present invention provides a semiconductor integrated circuit device equipped with at least one pulse generator which generates a pulse of a pulse with shorter
5 than a rising time up to the full amplitude of a transfer signal.

A first signal and a second signal supplied from outside through a first signal path and a second signal path are respectively transferred to the pulse generator. When
10 a rising time up to the full amplitude at any one of buffers in the first signal path and the second signal path is longer than a pulse width of a pulse to be formed by the pulse generator, the difference in phase between the first signal and the second signal is caused to correspond to a pulse
15 width of a first pulse.